



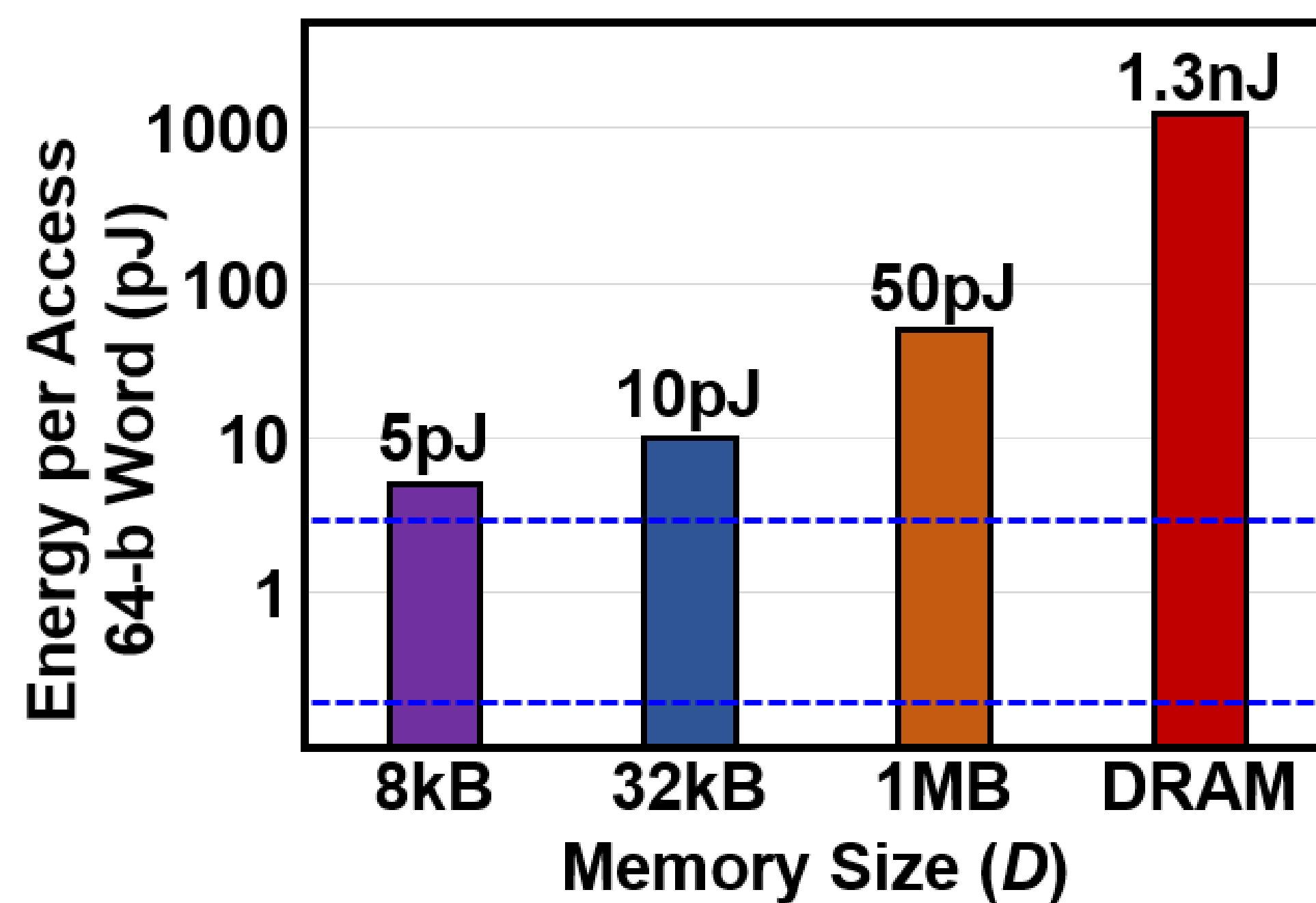
An eFlash-Based Computation-in-Memory for AI Edge Computing



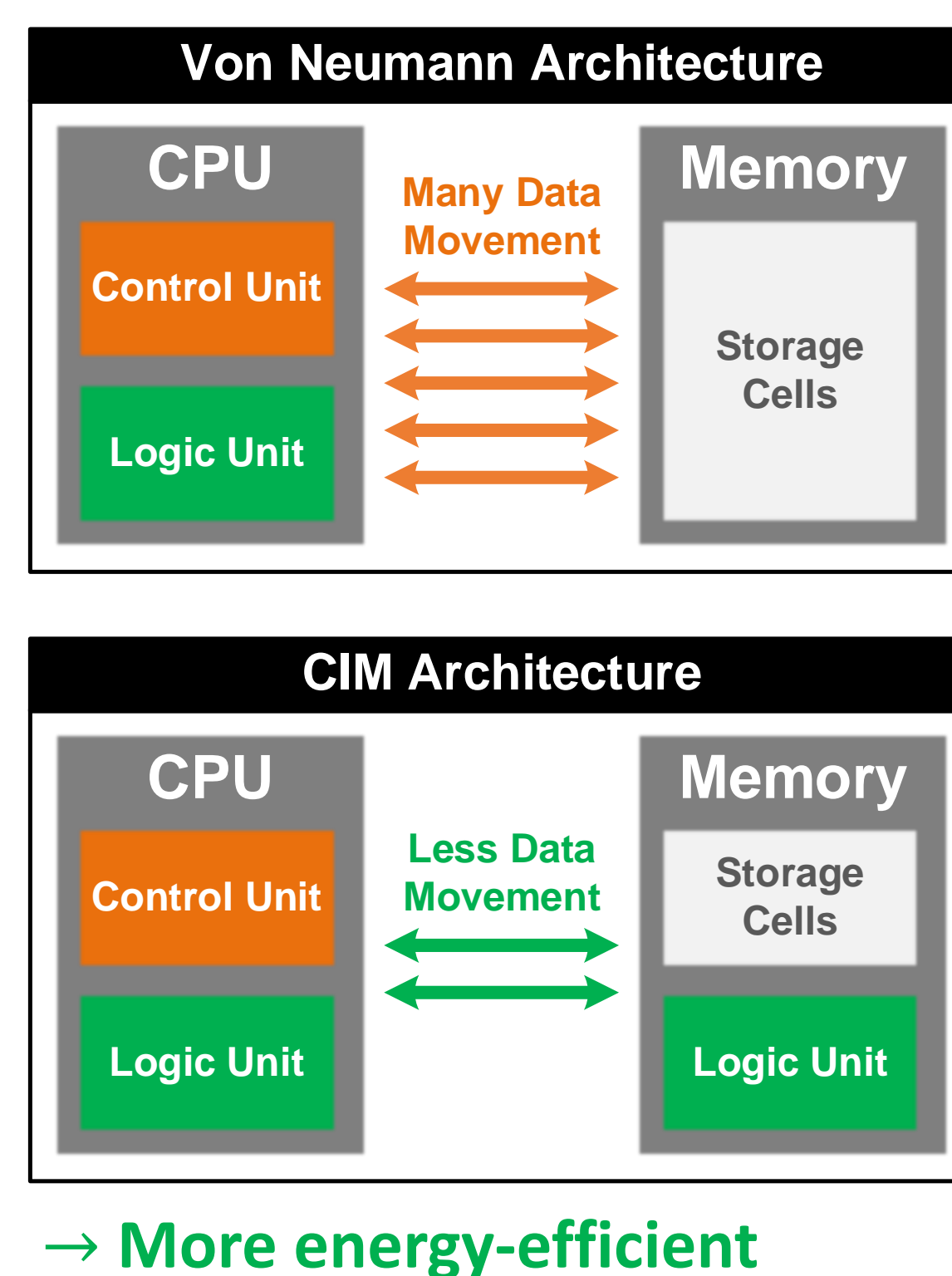
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Abstract

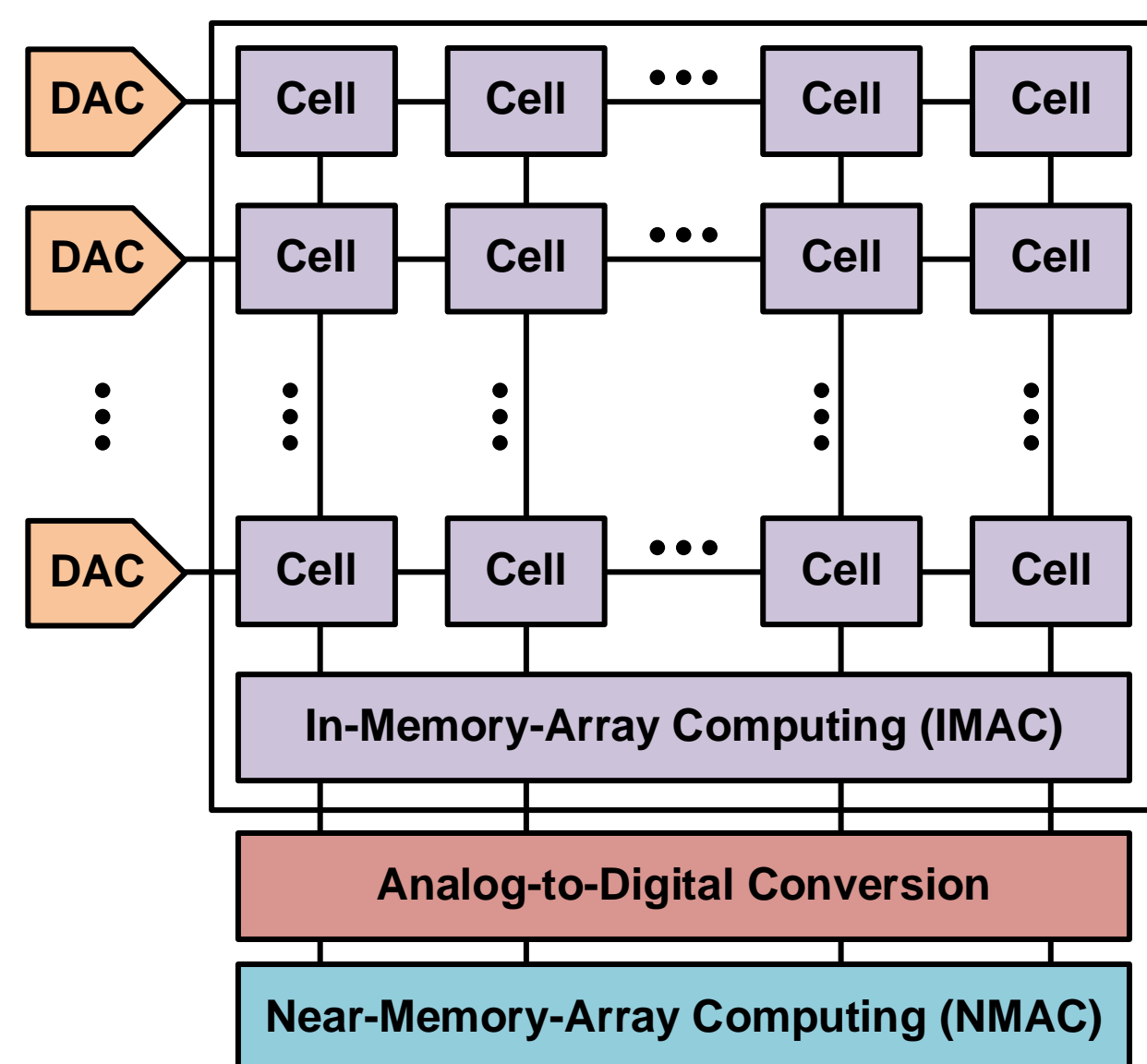


Memory access energy
» Computation energy^[1]

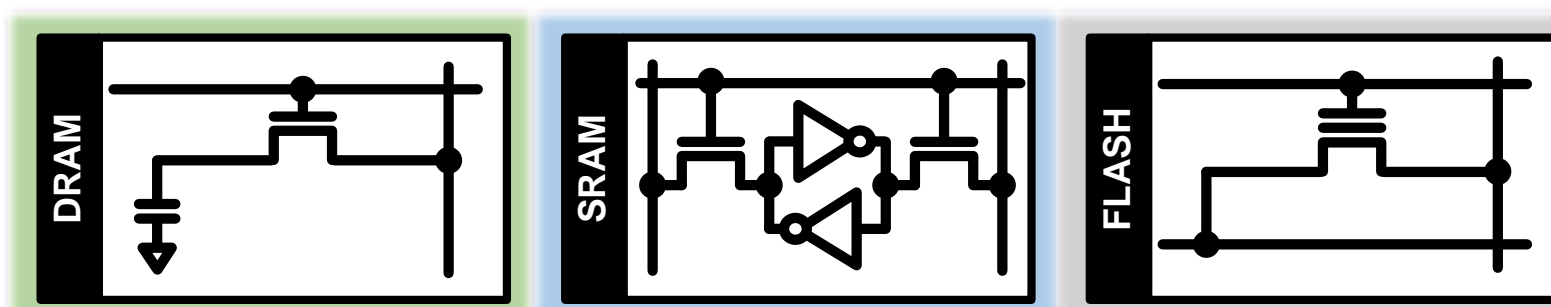


- To achieve energy-efficient AI computing
 - It is crucial to minimize the amount of data movement
- Memory access energy is greater than computing energy
 - Computation-in-Memory(CIM) can drastically reduce data movement and implement a more energy-efficient system
 - CIM is a memory-centered computing architecture that integrates computing units in memory
- CIM enables the implementation of energy-efficient systems
 - Reduce the movement of data
 - Effective for parallel processing

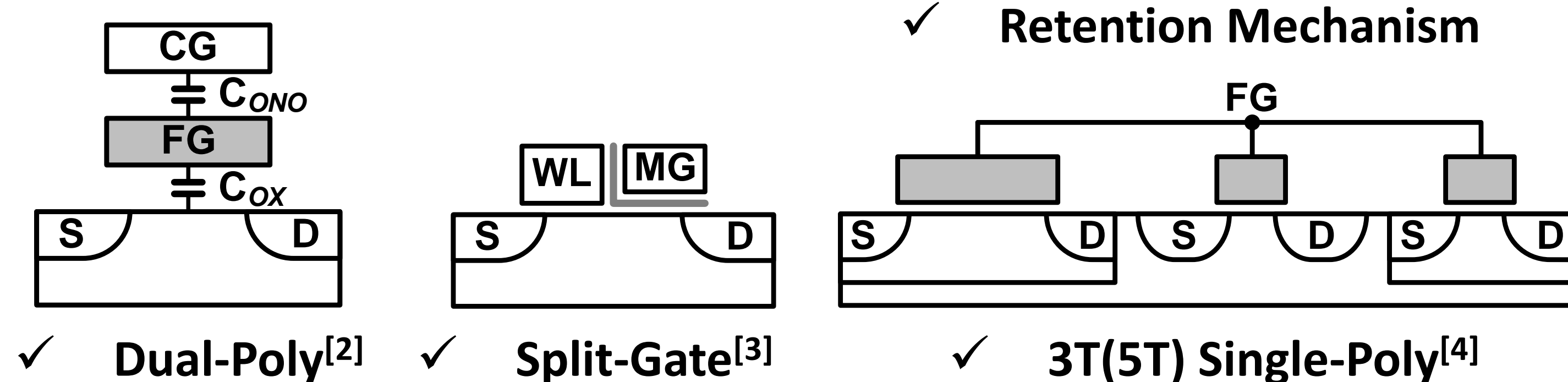
Introduction



- Dynamic data: Hard to use the stored data to compute
- SRAM: Provide fast read and write speed and low write energy
- NVM: The data does not require power to be maintained

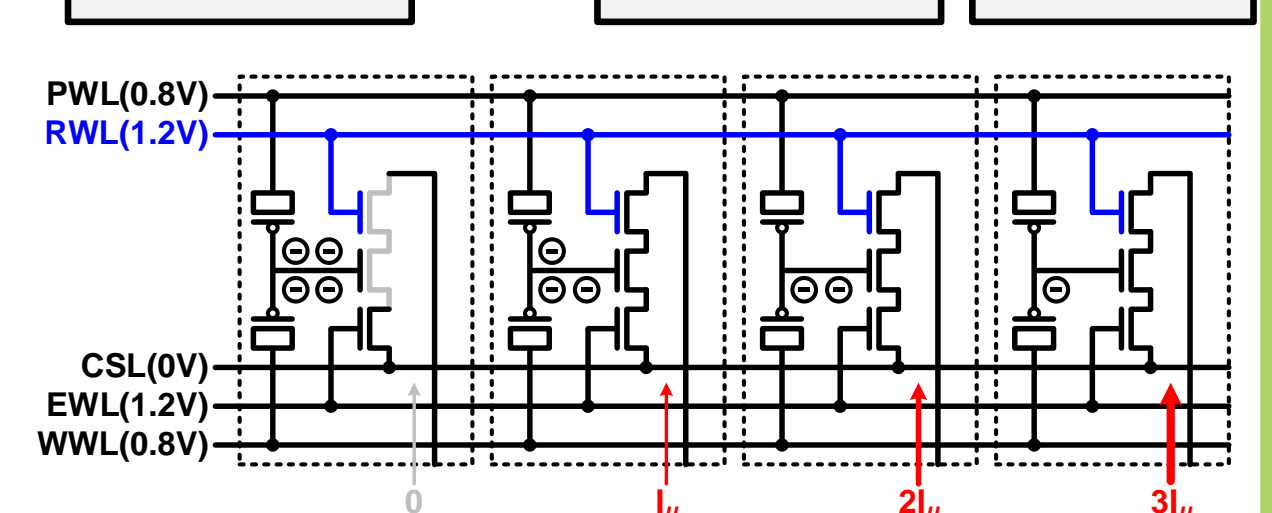
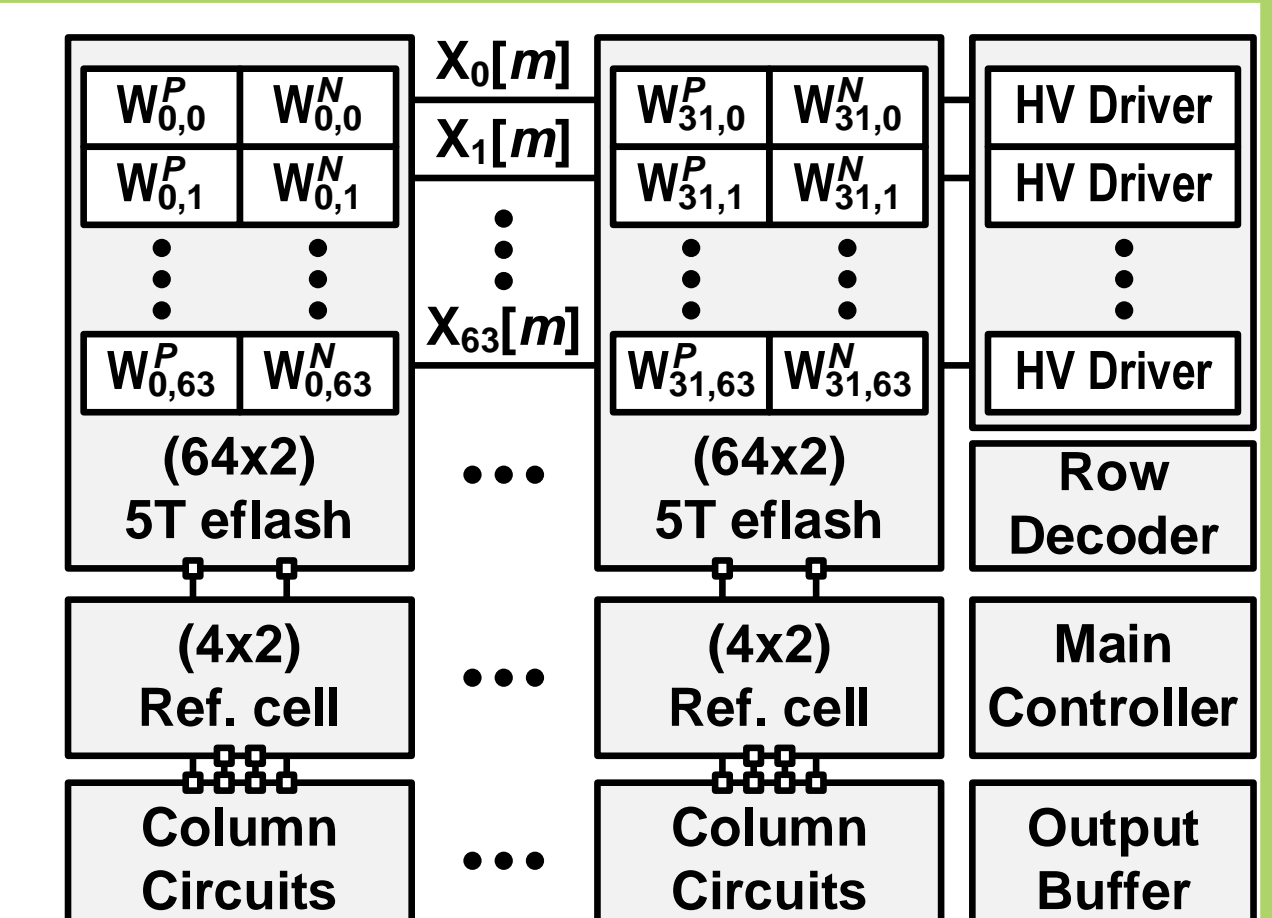
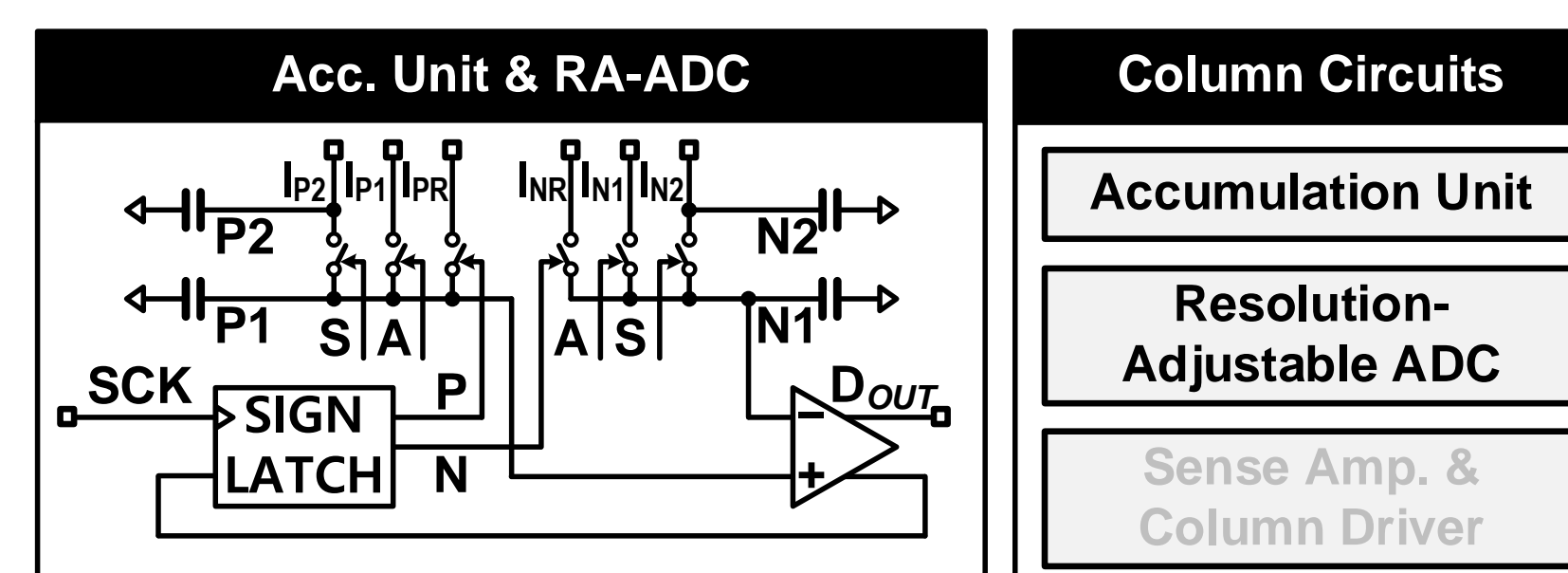


- Single-poly 3T eFlash cell
 - Low PGM voltage (~8V)
 - Low fabrication cost
 - High coupling rate

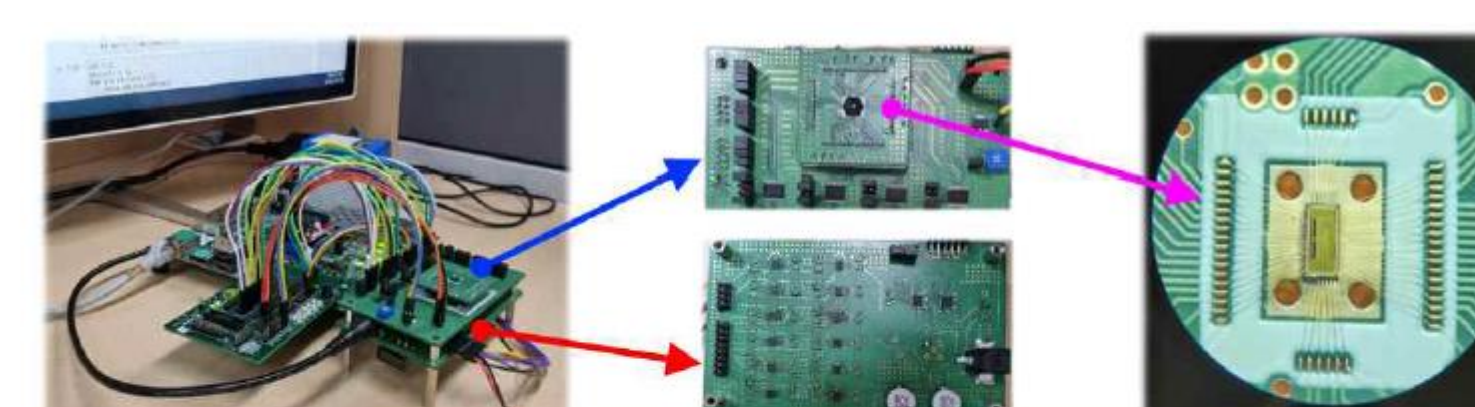


Implementation & Measurement

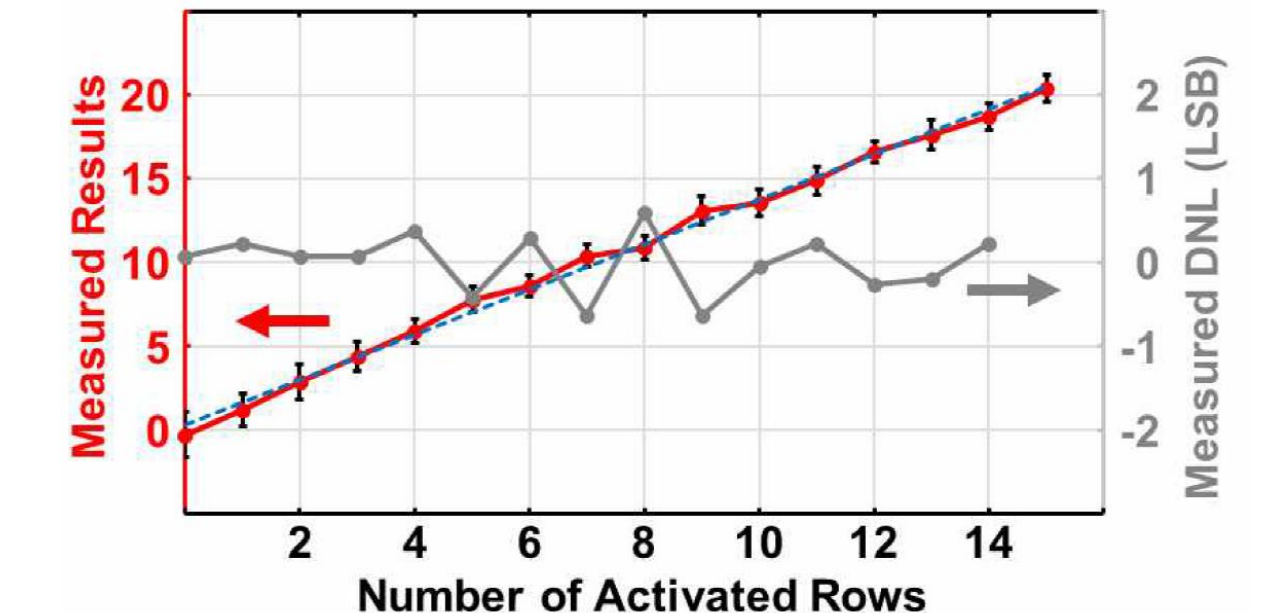
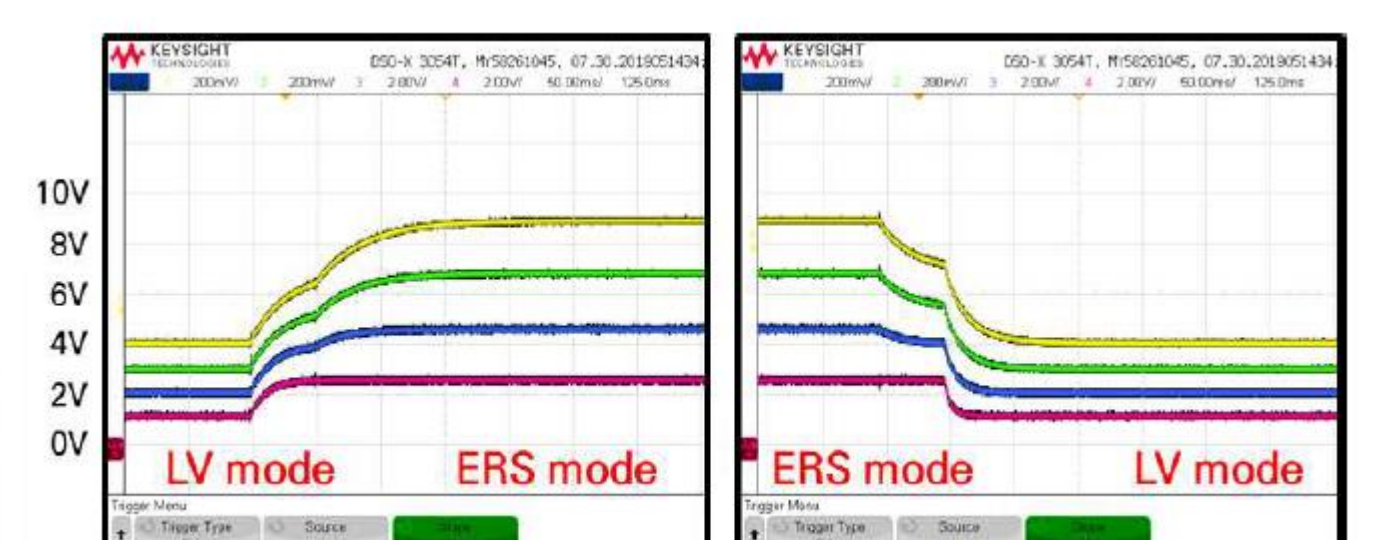
- High-level block diagram
 - Multi-level eflash pair
 - Biasing for wide-swing BL
 - Charge domain computing
 - Resolution-adjustable ADC



- Measurement results



- FPGA 기반의 측정 시스템
 - Xilinx ZCU104
 - Host interface
 - eFlash 컨트롤러
 - eFlash 기반 PIM 컨트롤러
- 메인 보드 & 전원 보드
 - 메인 보드: CoB 실장
 - level shifter, 전류원 등
 - 전원 보드: LDO 및 RDAC를 활용한 전원 관리
- CoB
 - PIM칩 실장 보드



Conclusion & Acknowledgement

- Challenges
 - BL Limited dynamic range and nonlinearity issues
 - ADC and DAC overhead
- Achievements
 - Low-cost charge domain computing circuit and ADC
 - Analyze MAC values for a sufficient margin

- References
 - [1] M. Horowitz, ISSCC 2014
 - [2] H. Kojima, IEDM 2007
 - [3] J. Yater, IMW 2009
 - [4] S.-H. Song, JSSC 2013

- Acknowledgement
 - The chip fabrication and EDA tool were supported by the IC Design Education Center(IDEC), Korea.